Docket No.: 60188-788 **PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Masashi AGATA

Confirmation Number:

Serial No.:

Group Art Unit:

Filed: March 02, 2004

Examiner:

For:

SEMICONDUCTOR MEMORY CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Mail Stop Information Disclosure Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E. Fo

Registration No. 36.139

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 MEF:prg Facsimile: (202) 756-8087

Date: March 2, 2004

									CC 1 _1	<u> </u>		
INFO	CIT	ΆT	ON DISCLO ION IN AN ICATION	ATTY. DOCKET NO. 60188-788		NO.						
	11.			APPLICANT Masashi AGATA								
		(PT	O-1449)	FILING DATE March 02, 2004	1			COUP				
				U.S. PATE	NT DOCUMENTS							
67444115516		1										
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code2 (# known)		Publication Da MM-DD-YYY		Name of Patentee or Applicant of Cited Document			Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
		US	6,111,802	08/29/2000	Kano et al.							
	1	US		,								
	1	US		-								
		US										
		US						·······	· ·			
	†	US		-				*				
	i	US	· · · · · · · · · · · · · · · · · · ·						 			
	i	US								• • • • • • • • • • • • • • • • • • • •		
		US										
	 	us					-					
	†	US										
	 	us										
	1	US		-								
		US										
	1	<u> </u>		FOREIGN P	ATENT DOCUMENTS							
EXAMINER'S		Foreign Patent Document Publication Date			Name of Patentee or	Pages.	Pages, Columns, Lines		Translation			
INITIALS	CITE NO.	Country Codes -Number 4 -Kind Codes (if known)		MM-DD-YYYY		Where Relevant Figures Appear						
	ļ					<u> </u>		Yes		No		
		<u> </u>										
		1		·		<u> </u>						
		1				<u> </u>						
		<u> </u>				<u> </u>						
EVALUEDIC	Υ	17			nor, Title, Date, Pertinent Pages, E			 		- ₁		
INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.										
		P.R. SCHROEDER et al., "A 16K x 1 Bit Dynamic RAM", ISSCC Digest of Technical Papers" U.S.A. ISSCC (International Solid-State Circuits Conference), February 1977, pages 12-13. J. BARTH, et al., "A 300MHz Multi-Banked eDRAM Macro Featuring GND Sense, Bit-Line Twisting and Direct Reference Cell Write", ISSCC Digest of Technical Papers, U.S.A. ISSCC (International Solid-State Circuits Conference), February 2002, pages 156-157.										
										†		
										 		
		EX	AMINER		DATE CONSIDERED							

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.